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10/721,673 11/24/2003 Wangguo Qin 188122002200 5631 25226 7590 04/19/2006 EXAMINER MORRISON & FOERSTER LLP KIK, PHALLAKA		FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.			
MORRISON & FOERSTER LLP KIK, PHALLAKA	10/721,673	11/24/2003	Wangguo Qin	188122002200	5631			
,	25226	590 04/19/2006		EXAM	EXAMINER			
755 PAGE MILLED	MORRISON & FOERSTER LLP 755 PAGE MILL RD		•	KIK, PHALLAKA				
			•	ART UNIT	PAPER NUMBER			
2825				2825				

DATE MAILED: 04/19/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

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		Application No.		Applicant(s)		
		10/721,673		QIN ET AL.	•	
	Office Action Summary	Examiner		Art Unit		_
		Phallaka Kik		2825		
Period fo	The MAILING DATE of this communication apport Reply	ears on the cover s	heet with the co	rrespondence ad	dress	
WHI(- Exte after - If NC - Failt Any	ORTENED STATUTORY PERIOD FOR REPLY CHEVER IS LONGER, FROM THE MAILING DATE of the may be available under the provisions of 37 CFR 1.1: SIX (6) MONTHS from the mailing date of this communication. O period for reply is specified above, the maximum statutory period vare to reply within the set or extended period for reply will, by statute reply received by the Office later than three months after the mailing ed patent term adjustment. See 37 CFR 1.704(b).	ATE OF THIS COM 36(a). In no event, however vill apply and will expire SIX , cause the application to be	MUNICATION. r, may a reply be time ((6) MONTHS from trecome ABANDONED	ly filed ne mailing date of this o (35 U.S.C. § 133).		
Status					•	
1)⊠ 2a)□ 3)□	Responsive to communication(s) filed on <u>24 M</u> . This action is FINAL . 2b) This Since this application is in condition for allowar closed in accordance with the practice under E	action is non-final.	al matters, pros	ecution as to the	e merits is	
Disnosit	ion of Claims					
5)□ 6)□ 7)□ 8)⊠	Claim(s) 1-70 is/are pending in the application. 4a) Of the above claim(s) is/are withdraw Claim(s) is/are allowed. Claim(s) is/are rejected. Claim(s) is/are objected to. Claim(s) 1-70 are subject to restriction and/or extended.	vn from considerati				
Applicat —	ion Papers					
10)□	The specification is objected to by the Examine The drawing(s) filed on is/are: a) accomplicant may not request that any objection to the Replacement drawing sheet(s) including the correct The oath or declaration is objected to by the Examine	epted or b) object drawing(s) be held in ion is required if the d	abeyance. See rawing(s) is obje	37 CFR 1.85(a). cted to. See 37 C	• •	
Priority (ınder 35 U.S.C. § 119					
a)l	Acknowledgment is made of a claim for foreign All b) Some * c) None of: 1. Certified copies of the priority documents 2. Certified copies of the priority documents 3. Copies of the certified copies of the priority application from the International Bureau See the attached detailed Office action for a list of	s have been receive s have been receive ity documents have ı (PCT Rule 17.2(a)	ed. ed in Application e been received).	n No I in this National	Stage	
	e of References Cited (PTO-892)	4) 🔯 <u>I</u> nti	erview Summary (F	PTO-413)	·	
3) 🔲 Inforr	e of Draftsperson's Patent Drawing Review (PTO-948) mation Disclosure Statement(s) (PTO-1449 or PTO/SB/08) r No(s)/Mail Date	5) 🔲 No	per No(s)/Mail Date tice of Informal Pat ner:	e. <u>20060414</u> . ent Application (PTC	D-152)	

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DETAILED ACTION

1. This Office Action responds to the Application filed on 11/24/2003 and interview conducted on 4/10/2006.

Restriction

- 2. Restriction to one of the following inventions is required under 35 U.S.C. 121:
 - I. Claims 1-62,70, drawn to method/computer programmed system/computer executable software code for transforming a circuit from a first topology to a reduced topology, classified in class 716, subclass 3.
 - II. Claims 63-67, drawn to a method of transforming a first circuit from a first topology to a reduced topology involving reducing the circuit elements in a bottom-up fashion from the leaf nodes to the root of the tree-like topological approximation that was generated and identified, classified in class 716, subclass 3.
 - III. Claim 68, drawn to a method of reducing capacitors within a circuit topology involving moving and merging capacitors identified associated with the particular connections to the transistor drain or source node, classified in class 716, subclass 2.
 - IV. Claim 69, drawn to a method of reducing nodes within a circuit topology involving merging nodes P1 and P2 into one node if P1 and P2 satisfy the particular conditions, classified in class 716, subclass 2.
- 3. The inventions are distinct, each from the other because of the following reasons:

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Inventions III and IV are related as subcombinations disclosed as usable together in a single combination. The subcombinations are distinct if they do not overlap in scope and are not obvious variants, and if it is shown that at least one subcombination is separately usable. In the instant case, subcombination IV has separate utility such as reducing circuits with the particular capacitors and resistors connections that are not covered by the transistor related connections of subcombination III, and further wherein the subcombinations do not overlap in scope or are not obvious variants, since the reduction steps are applied differently to the particular capacitors and transistors connections recited in each invention. See MPEP § 806.05(d).

Inventions I and II are directed to related processes. The related inventions are distinct if the inventions as claimed do not overlap in scope, i.e., are mutually exclusive; the inventions as claimed are not obvious variants; and the inventions as claimed are either not capable of use together or can have a materially different design, mode of operation, function, or effect. See MPEP § 806.05(j). In the instant case, the inventions as claimed do not overlap in scope and are not obvious variants of each other and have different mode of operation, because the reducing of the circuit elements in a bottom-up fashion from the leaf nodes to the root of the tree-like topological approximation as claimed in invention II do not overlap in scope and is not an obvious variants of invention I, which includes producing the topological approximation as a minimum spanning tree and identifying one or more symmetric nodes in a top-down fashion from the single input tree structure (see claims 5 and 7), and would therefore operate differently from the reducing steps/means of invention I.

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Inventions I,II and III,IV are related as combination and subcombination.

Inventions in this relationship are distinct if it can be shown that (1) the combination as claimed does not require the particulars of the subcombination as claimed for patentability, and (2) that the subcombination has utility by itself or in other combinations (MPEP § 806.05(c)). In the instant case, the combination as claimed does not require the particulars of the subcombination as claimed because the method/computer programmed system/computer executable software code for transforming a circuit from a first topology to a reduced topology of inventions I and II could still operate for the particular circuit elements and their connections not involving those as recited in invention III and IV. The subcombination has separate utility such as specifically reducing the circuit topology involving the particular capacitors and resistors in the particular connections as recited in inventions III and IV.

- 4. Because these inventions are independent or distinct for the reasons given above and the inventions require a different field of search (see MPEP § 808.02), restriction for examination purposes as indicated is proper.
- 5. A telephone call was made to Thomas Chan (Reg. No. 51,543) on 4/10/2006 to request an oral election to the above restriction requirement, but did not result in an election being made.
- 6. Applicant is advised that the reply to this requirement to be complete must include (i) an election of a species or invention to be examined even though the requirement be traversed (37 CFR 1.143) and (ii) identification of the claims encompassing the elected invention.

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7. The election of an invention or species may be made with or without traverse. To reserve a right to petition, the election must be made with traverse. If the reply does not distinctly and specifically point out supposed errors in the restriction requirement, the election shall be treated as an election without traverse.

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- 8. Should applicant traverse on the ground that the inventions or species are not patentably distinct, applicant should submit evidence or identify such evidence now of record showing the inventions or species to be obvious variants or clearly admit on the record that this is the case. In either instance, if the examiner finds one of the inventions unpatentable over the prior art, the evidence or admission may be used in a rejection under 35 U.S.C.103(a) of the other invention.
- 9. Applicant is reminded that upon the cancellation of claims to a non-elected invention, the inventorship must be amended in compliance with 37 CFR 1.48(b) if one or more of the currently named inventors is no longer an inventor of at least one claim remaining in the application. Any amendment of inventorship must be accompanied by a request under 37 CFR 1.48(b) and by the fee required under 37 CFR 1.17(i).
- 10. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Phallaka Kik whose telephone number is 571-272-1895. The examiner can normally be reached on Monday-Thursday, 8:30AM-7PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Jack Chiang can be reached on 571-272-7483. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Any response to this action should be mailed to:

Commissioner for Patents

P. O. Box 1450

Alexandria, VA 22313-1450

or faxed to:

571-273-8300

Phallaka Kik

U.S. Patent Examiner

April 14, 2006